

CAD-UL LINK86 Cross Linker and Locator

- Linkable and Absolute OMF86
- Storage Models: SMALL, SMALL-ROM, COMPACT, MEDIUM and LARGE
- Cross Reference Listing
- Supports Bank Switching Models
- Full Support for Subsystems
- ROM-able Code
- Incremental Linking
- Supports Shareable Libraries

CAD-UL Link86 combines input modules (Linkable OMF86 Format) into a single output module. Available output module formats include Intel Linkable OMF86, Load-Time-Loadable (LTL) and Absolute-Loadable OMF86. Linker and builder functions are implemented in the single CAD-UL LINK86 tool and, if required, the linker can be called by itself to support incremental linking. Incremental linking enables applications to be generated for operating systems such as RMOS 2, iRMX and pSOS as

well as for pure embedded systems. CAD-UL LINK86 is part of the CAD-UL development system as well as being fully Intel-compatible. This combination gives the user the advantages of an integrated tool chain and the flexibility that comes with industry standard compatibility.

CAD-UL Cross Linker LINK86 generates Intel OMF86 Object Format for code, symbol and debug information and thus permits use of existing in-circuit emulators. Intel build files can be loaded and processed without modification. These features permit a trouble free transition to CAD-UL tools.

PROCESSORS SUPPORTED:
Intel 80186 processors

DEVELOPMENT PLATFORMS:
MS-DOS, MS-Windows* 3.1x, Windows 95, Windows NT, OS/2, SCO-UNIX, Interactive UNIX, Sun (Solaris 86, 1x, 2x), HP 9000/700 - and other UNIX hosts, as well as DEC VAX VMS and Alpha VMS

AVAILABILITY:
Now

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